



HW/SW  
EMULATION



## [Aldec Delivers Verification Support for Embedded Applications with New TySOM™ Embedded Development Kit](#)

Henderson, NV – August 16, 2016 – [Aldec, Inc.](#), a pioneer in mixed-HDL language simulation and hardware-assisted verification solutions for FPGAs and ASICs, today announced the release of [TySOM™ Embedded Development Kit](#) (EDK). The TySOM EDK targets embedded designers who require a platform with higher-performance RTL simulation and debugging for developing leading-edge embedded applications for IoT, Computer Vision, Automotive, Robotics and Factory Automation.

The kit includes [Riviera-PRO™ Advanced Verification Platform](#), a Zynq™-based development board and pre-validated Ubuntu™ Embedded Host reference designs and tutorials.

- Riviera-PRO offers mixed-HDL language simulation for VHDL 2008/Verilog 2005 and includes advanced debugging features such as Dataflow, Xtrace, Post-Simulation Debug and Code Coverage critical in pinpointing the source of errors during SoC system integration.
- The TySOM board includes a single Zynq chip (FPGA fabric + Dual ARM® Cortex-A9), memories (DDR3, uSD), communication interfaces (miniPCIe, Ethernet, USB, FMC, Pmod™, JTAG) and multimedia interfaces (HDMI, audio, CMOS camera).
- The Ubuntu Embedded Host reference design and tutorials transform the TySOM board into a fully-stacked Linux box where users are able to learn the basics of the main Linux OS concepts and frameworks, and as well as compiling and executing complex Linux applications.

“Aldec’s 30+ years verification expertise and simulation technology for FPGAs provide embedded designers the tools they need for their high-performance embedded products,” said Louie De Luna, Director of Marketing. “As new embedded technologies emerge for video stream processing, security encryption or sensor data computation the need for a high-performance RTL simulation/verification platform has never been greater.”

“We observed a large increase of FPGA-based embedded projects, and based on our simulation and FPGA expertise it was a natural step for Aldec to introduce an embedded development kit,” said Zibi Zalewski, General Manager Hardware Products Division. “What is important here is that the TySOM kit targets not only software developers, but also hardware designers with multiple reference designs merging Linux port with a variety of peripherals and connectivity to external devices. SoC projects in FPGA have become a reality, with ARM CPUs in the FPGA for firmware and a direct link to reprogrammable logic for hardware controllers and computing acceleration.”

#### **Availability**

Aldec is now shipping the TySOM Embedded Development Kit in two configurations. **For additional information, including Technical Papers, Demo Videos and an Overview Presentation, visit <https://www.aldec.com/products/tysom>.**

#### **About Aldec**

Aldec Inc., headquartered in Henderson, Nevada, is an industry leader in Electronic Design Verification and offers a patented technology suite including: RTL Design, RTL Simulators, Hardware-Assisted Verification, SoC and ASIC Prototyping, Design Rule Checking, IP Cores, Embedded, Requirements Lifecycle Management, DO-254 Functional Verification and Military/Aerospace solutions. [www.aldec.com](http://www.aldec.com)

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Avant Technology Inc.

Sales Contact (Taiwan)

Jill Chen

+886-3-668-6603

[sales@avant-tek.com](mailto:sales@avant-tek.com)